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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/870,447	06/01/2001	Brian Boles	18153.0045	8455
31625	7590	11/10/2005	EXAMINER	
BAKER BOTTS L.L.P. PATENT DEPARTMENT 98 SAN JACINTO BLVD., SUITE 1500 AUSTIN, TX 78701-4039				CLEARY, THOMAS J
ART UNIT		PAPER NUMBER		
		2111		

DATE MAILED: 11/10/2005

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Serial No. : 09870447  
Applicant : Brian Boles  
Filing Date : June 1, 2001  
Date Mailed : November 10, 2005

## ACKNOWLEDGEMENT OF REQUEST

### *Notice of Allowance/Allowability Mailed*

The request for a copy of the initialed PTO 1449, dated December 10, 2004, has been received by the U.S. Patent and Trademark Office.

- Requested copy attached

SUNDA M. ARCHER  
For the Office of Patent Publication

PTO-1449		Application No.	Applicant(s)			
		09/870,447	Brian Boles et al.			
		Docket Number	Group Art Unit	Filing Date		
		068354.1471	2111	June 1, 2001		
<b>U.S. PATENT DOCUMENTS</b>						
	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.	3930253	12/30/75	Maida	340	347	1/24/74
B.	5974549	10/26/99	Golan	713	200	3/27/97
C.	6055619	4/25/00	North et al.	713	36	2/7/97
D.	6282637	8/28/01	Chan et al.	712	223	12/2/98
E.	6728856	4/27/04	Grosbach et al.	711	202	6/1/01
F.						
G.						
H.						
I.						
J.						
<b>FOREIGN PATENT DOCUMENTS</b>						
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
						YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
K.	01037424 A	2/89	JP	H03M	001/82	X
L.	0 554 917 A2	8/11/93	EP	G06F	9/26	X
M.	0 855 643 A1	7/29/98	EP	G06F	9/30	X
N.	0 992 888	12/4/00	EP	G06F	9/32	X
O.	0 992 889	12/14/00	EP	G06F	9/32	X
P.	96/11443	4/18/96	WO	G06F	15/78	X
<b>NON-PATENT DOCUMENTS</b>						
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
Q.	Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0916-8508					5/2001
R.	Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, US, vol. 37, no. 10, pages 46, 48, 50-53, XP000860706 ISSN:0010-4566					10/1998
S.	Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, pages 73-74, 76, 78-80, XP000779113 ISSN: 0012-7515					3/2/1998
T.	Intel, Pentium Processor Family Developer's Manual, Volume 3: Architecture and Programming Manual, , Pages 3-1, 3-2, 3-15, 14-1 to 14-30, 18-7, and 25-289 to 25-292					1995
U.	Intel, Embedded Intel486 Processor Family Developer's Manual, pgs. 2-2, 3-17, 3-37, 4-5, 4-6, 10-1 to 10-12, 12-1 to 12-10					10/1997
EXAMINER						DATE CONSIDERED
<i>Thurley</i>						<i>10-June-05</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.						